

REMARKS

The specification has been amended to correct minor obvious errors. A marked up version of the amended paragraphs of the specification is attached hereto pursuant to 37 C.F.R. § 1.121(b)(iii). Claim 1 has been amended for clarity. A marked up version of the amended claim is also attached hereto pursuant to 37 C.F.R. § 1.121(c)(ii). New claims 7-20 have been added. Claims 2-6 remain unchanged. Thus, claims 1-20 are presently pending in this application for consideration.

The amendments to the present application are made to place the application in better form and to place the application in condition for allowance. No new matter has been added. Entry and consideration of these amendments prior to the first Office Action are respectfully requested.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at Los Angeles, California, telephone number (213) 337-6742 to discuss the steps necessary for placing the application in condition for allowance.

If there are any fees due in connection with the filing of this response, please charge the fees to our Deposit Account No. 50-1314.

Respectfully submitted,

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Date: July 25, 2002

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Version with markings to show changes made:

IN THE SPECIFICATION

Please amend the paragraph on page 3, starting at line 2 as follows:

The above and other [objects,] features [,] and advantages of the present invention will become further apparent from the following description of the preferred embodiment taken in conjunction with the accompanying drawings wherein:

IN THE CLAIMS:

Please amend the claims as indicated below:

1. (Once Amended) An interface circuit for USB data transfer, comprising:

a power source determination circuit for determining a type of power source;

a power feeding switching circuit for setting an amount of current to be supplied to a processing circuit conducting processing for data transfer, based on a result of determination made by the power source determination circuit [means]; and

a clock switching circuit for setting operation clocks for the processing circuit according to a result of determination made by the power determination circuit [means],

wherein a data transfer rate is set according to the type of power source.